

**IN THE SPECIFICATION**

Page 16, the first full paragraph, lines 13 to 24,  
replace the paragraph with:

Applying the control voltage  $V_{cnt1}$  from the phase comparison circuit—~~136~~ 137 shown in Fig.1 to the node n10 of R11 and R12 determines the carrier frequency, and applying the control voltage  $V_{cnt2}$  from the low-pass filter 133 shown in Fig.1 to the node n20 of R21 and R22 controls the modulation frequency. Based on the generation of a stable and accurate carrier frequency with this closed PLL circuit and the modulation of the frequency in accordance with the external control voltage, the circuit arrangement can be simpler and the circuit area can be smaller as compared with the control scheme of feeding a modulation signal into the loop of PLL circuit.